

Hardware Security Module (HSM) of the AURIX™ Platform - Live Online Training

Objectives

You know the architecture, on-chip peripherals and features (especially related to the host aspects) of the HSM module of the AURIX™ device family.

You get to write and apply low-level drivers for this hardware, interact with the host, adapt examples as required and test them with a debugger.

Numerous demos and exercises intensify the theoretic content.

YOUR BENEFIT:

Efficient and compact jump-start into the overall topic

System-wide approach

Training documentation in electronic format

Participants

Hardware and software architects, hardware and software developers, test engineers, design engineers, system designers

Requirements

HSM NDA (non-disclosure agreement) with Infineon; experience in microcontroller/microprocessor system programming and architecture; basic security knowledge; AURIX system knowledge (ideally, based on our AURIX-2G training)

Live-Online-Training

* Price per attendee, in Euro plus VAT

Training code: LE-HSM

Face-To-Face - English

Date **Duration**

18.06. – 19.06.2026 2 days

Live Online - German

Duration

2 days

Face-To-Face - German

Date **Duration**

18.06. – 19.06.2026 2 days

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Content

Introduction

Inside Hardware Security Module

CPU Subsystem Overview

System Aspects (Configuration, Boot, Reset, Debug)

Bridge

Timer Module and Watchdog

True Random Number Generator

Hash Module

Advanced Encryption Standard - 128 bit (AES-128)

Public Key Cryptography (PKC) Module

IMPORTANT NOTE:

A valid HSM NDA (non-disclosure agreement) with Infineon is a pre-requirement to attend the course.