

## Multicore-Debug for TriCore/AURIX™ with Lauterbach TRACE32 - Live Online Training

### Objectives

This training addresses newcomers to the area of debugging with the Lauterbach TRACE32 toolchain as well as users who want to extend their knowledge, for example, of multicore target platforms. Generic and AURIX™ specific features are highlighted and demonstrated, also in the context of the Lauterbach scripting language Practice. From generating fundamental configuration scripts across all classic debug methods to more advanced techniques, like automation through Python and operating system based debugging, attendees will extend and intensify their knowledge. They work on fully functional target platforms enabling them to directly apply and reproduce what they just learned.

### Participants

Hardware and software developers, test engineers, verification engineers, application engineers, integrators

### Requirements

Basic knowledge of the AURIX™ microcontroller architecture

### Live-Online-Training

16.07. – 16.07.2026 700,00 € 1 Days

\* Price per attendee, in Euro plus VAT

Training code: LE-T32-BAS

### Face-To-Face - English

**Date**                   **Duration**

26.03. – 26.03.2026 1 day

### Live Online - German

**Date**                   **Duration**

16.07. – 16.07.2026 1 day

### Face-To-Face - German

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### Content

**AURIX™ Debug Basics**

**T32 Intro**

**Practice: Scripting Language Aspects**

**Startup Scripts - Multicore (SMP, AMP)**

**"Housekeeping" Features**

**System Command Group**

**Special AURIX™ Features**

**Views (Registers, Peripherals, Memory, Variables, Code)**

**Call Stack**

**Breakpoints**

**Symbol Database**

**Source Path Adjustments**

**Basic Measurements (RunTime, Benchmark Counters, CLOCK)**

**Unit Test**

**Automation with Python**

**OS Awareness**