

Tracing and Multicore Debugging for TriCore/AURIX™ (MCDS) with the PLS Universal Debug Engine UDE - Live Online Training

Objectives

You can control the trace recording for AURIX™ multicore systems and evaluate it. In particular, this includes the runtime time measurement of functions and code coverage analysis.

Participants

Hardware and software developers, test managers, test engineers

Requirements

Basic knowledge of AURIX™ microcontroller multicore architectures; basic knowledge of debugging with the PLS debugger (e.g. from the training "Debugging for TriCore/AURIX™ with the PLS Universal Debug Engine UDE") is required.

Live-Online-Training

* Price per attendee, in Euro plus VAT

Training code: LE-MCDSPLS

Face-To-Face - English

Duration

1 day

Live Online - German

Duration

1 day

Face-To-Face - German

Duration

1 day

Tracing and Multicore Debugging for TriCore/AURIX™ (MCDS) with the PLS Universal Debug Engine UDE - Live Online Training

Content

Trace - Basic Knowledge

- Trace protocol description
- On-chip versus off-chip trace

Trace Configuration with PLS UDE

- Trace sources and trace messages
- Tracing of multicore systems

Displaying Trace Information

- Trace configuration window
- Trace list window
- Trace control with filter and trigger settings

Optional: OS-aware Trace

- Configuration of OS-aware tracing
- Exporting OS task switches and IRS
- Runtime analysis for your OS

Runtime Analysis

- Flat function runtime analysis
- Nesting function analysis

Trace-based Code Coverage**Optional: Bosch GTM Module Tracing**