

Tracing and Multicore Debugging for TriCore/AURIX™ (MCDS) with the PLS Universal Debug Engine UDE - Face-to-Face Training

Objectives

You can efficiently use the PLS UDE for debugging and tracing an AURIX™ multicore system. This includes all basic methods (e.g. breakpoints, run control, watch, registers, peripherals, memory, etc.), extended functions, like various methods of runtime measurement or code coverage analysis as well as hardware-based (MCDS) trace recording and the related evaluation.

YOUR BENEFIT:

We are working with real training systems. The trainer demonstrates the different features on a system, while the attendees can immediately reproduce and try out what they just learned on their training systems.

Participants

Hardware and software developers, test managers, test engineers

Requirements

Basic knowledge of the AURIX™ microcontroller architecture

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Content

Tool Architecture

Watching and Changing Registers

Watch Window (Variables)

Expressions

Locals and Call Stack

Memory Content

Graphic Display

Run Control

Runtime Measurement

Profiling

Multicore Debugging (Load, Run, Break, Cache, MPU)

Automation - Overview (e.g. Python)

Trace Recording

Configuration (Compact, Advanced)

Trace-Based Profiling

Data Trace

Code Coverage

Execution Sequences**Call Graph****GTM Debug/Trace****Peripheral Trace****FACE-TO-FACE TRAINING**

Date	Price *	Duration
26.11.2024 – 26.11.2024	700,00 €	1 day
15.07.2025 – 15.07.2025	700,00 €	1 day
03.03.2026 – 03.03.2026	700,00 €	1 day

* Price per attendee, in Euro plus VAT

Training code: E-MCDSPLS

Live Online - English

Date	Duration
18.02. – 18.02.2025	1 day
11.11. – 11.11.2025	1 day

Face-To-Face - German

Date	Duration
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Coaching

Our coaching services offer a major advantage: our specialists introduce their expertise and experience directly in your solution process, thus contributing to the success of your projects.

We will be happy to provide you with further information or submit a quotation tailored to your requirements.