

STM32: Technical Training - Face-to-Face Training

Objectives

You know the Cortex®-M3 architecture STMicroelectronics STM32F10x and are able to create drivers for the on-chip periphery.

Participants

Software and hardware developers

Requirements

Knowledge of ANSI-C as well as basic knowledge of microcontrollers.

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Content

Overview of the STM32 Architecture (STM32F0, STM32F1, STM32F2, STM32F3, STM32F4 and STM32F7 series) for Arm Cortex®-M0/M0+, Arm Cortex®-M3, Arm Cortex®-M4 and Arm Cortex®-M7 processors

CPU, Registers

Internal Bus Architecture

Stack Handling

Memory Mapping and Boot Modes

System Architecture

On-Chip Flash Architecture

Direct Memory Access Controller DMA

Power Supply, Clock Control, Reset

Power Management, Power Saving Modes

Real-Time Clock RTC

Window Watchdog WWDG, Independent Watchdog

Port Architecture: GPIO/AFIO

External Interrupt/Event Controller EXTI

Analog-to-Digital Converter ADC

Advanced Control and General Purpose Timers

Serial Peripheral Interface SPI

Inter Integrated Circuit IIC

Universal Synchr. Asynchr. Receiver Transmitter USART

Controller Area Network bxCAN

STM32 Cube HAL- and LL-Library

To intensify the content of the training, exercises will be performed regarding interrupts, DMA, timer, serial interface

and ADC.

- Exercises will be based on the - STM32 NUCLEO-F746ZG board by ST.
- The exercises are done using Keil Studio (Visual Studio Code), STM32CubeIDE (Eclipse) or STM32CubeIDE for Visual Studio Code.

FACE-TO-FACE TRAINING

Price *	Duration
2.100,00 €	3 days
Training code: E-STM32	
* Price per attendee, in Euro plus VAT	

Face-To-Face - German

Date	Duration
23.11. – 25.11.2026	3 days

Live Online - German

Date	Duration
13.07. – 15.07.2026	3 days

Coaching

Our coaching services offer a major advantage: our specialists introduce their expertise and experience directly in your solution process, thus contributing to the success of your projects.

We will be happy to provide you with further information or submit a quotation tailored to your requirements.