

Tracing and Multicore Debugging for TriCore/AURIX™ (MCDS) with Lauterbach TRACE32

Objectives

You can control the trace recording for AURIX™ multicore systems and evaluate it. In particular, this includes the runtime time measurement of functions and code coverage analysis.

Participants

Hardware and software architects, hardware and software developers, test engineers

Requirements

Basic knowledge of AURIX™ microcontroller multicore architectures; basic knowledge of debugging with TRACE32 (TRACE32 Basic Debugging Training)

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Content

Trace - Basic Knowledge

- Trace protocol description
- On-chip versus off-chip trace
- AMP versus SMP

Trace Configuration within TRACE32

- Trace sources and trace messages
- Tracing of multicore systems

Displaying Trace Information

- Trace configuration window
- Trace list window
- Trace control by filter and trigger

Optional: OS-aware Trace

- Configuration of OS-aware tracing
- Exporting OS task switches and IRS
- Runtime analysis for your OS

Runtime Analysis

- Flat function runtime analysis
- Nesting function analysis

Trace-based Code Coverage

Optional: Bosch GTM Module Tracing

Trainings

Price *	Duration
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-	1 day
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Training code: E-T32TRACE

* Price per attendee, in Euro plus VAT

Coaching

Our coaching services offer a major advantage: our specialists introduce their expertise and experience directly in

your solution process, thus contributing to the success of your projects.

We will be happy to provide you with further information or submit a quotation tailored to your requirements.